

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. WATANABE, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 10/145,810, filed May 16, 2002

Filed: On Even Date Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUITRY AND
METHOD FOR MANUFACTURING THE CIRCUITRY

Art Group of Parent: 2818

Examiner of Parent: D. Nhu

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 20, 2004

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Specification; and

Remarks are included following the amendments.